EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	81	detect$3 and ((error condition or condition or pass/fail condition) same (TAP or test access port) same (first TAP control device or TAP controller or test controller))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 16:43
L2	1	communicat$3 near2 (TAP or test access port) same (second TAP control device or second TAP controller or TAP master controller)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 16:48
L3	9653	second TAP controller or second test controller or master controller or TAP master controller	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 16:48
L4	13592	first TAP controller or first test controller or master controller or TAP master controller or TAP controller or test controller	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 16:49
L5	13592	(select$3 or MUX or multiplex$3 or control$4 or switch$3) and (l3 or l4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 16:53
L6	9653	(select$3 or MUX or multiplex$3 or control$4 or switch$3) and (l3 and l4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 16:53
L7	1	l1 and l6	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 16:54

L8	81	l1 and l5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 16:53
L9	573	integrated circuit same (TAP same logic)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 16:54
L10	21	l8 and l9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 17:24
L11	140449	test result$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 16:55
L12	8	l10 and l11	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 16:56
L13	5186	detected condition$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 16:55
L15	6	detected test condition$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 17:18

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L16	64	detected test result$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 16:56
L18	16987	714/724 or 714/733 or 714/734 or 714/? or 716/4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 17:26
L19	12	l18 and l10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 17:27
L20	18116	l18 or 714/726 or 714/725 or 714/727 or 714/729	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 17:28
L21	15	l10 and l20	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 17:28
L22	12	l19 and l20	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/27 17:45
L23	0	(integrated circuit AND TAP AND first TAP control device AND second TAP control device AND test condition AND failure AND multiplexer AND MUX AND test access port AND input node AND signal node AND test vector AND test pattern).clm.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/04/27 17:49